



# The 30<sup>th</sup> International Conference on Microelectronics (ICM 2018)

Call for Papers

<http://www.ieee-icm2018.org>

16-19 December 2018, Tunisia



## Honorary Co-Chairs

**Lotfi Kamoun**, ENIS, Tunisia  
**Mohamed Elmasry**, UoW, Canada

## General Co-Chairs

**Mohamed Abid**, ENIS, Tunisia  
**Mohamad Sawan**, PM, Canada

## Technical Prog. Co-Chairs

**Mourad Loulou**, ENIS, Tunisia  
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**Hassen Mnif**, ENETCom, Tunisia  
**Eric Kerhervé**, IMS, France  
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## Tutorials Co-Chairs

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**Mohammad Baker**, KH. Univ. AEU  
**A. Al Maashari**, SQ Univ. Oman

## Panels Co-Chairs

**Mohammed Ismail**, WS. Univ. USA  
**Amine Bermak**, HBK Univ. Qatar  
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## Finance Chair

**Kais Loukil**, ESCS, Tunisia  
**Tarek Ouni**, ENETCom, Tunisia

## Local Arrangement Members

**Wassim Jmal, Chair**, ISSATG, Tunisia  
Agnes Ghorbel, ENIS, Tunisia  
Amina Magdich, FSEGS, Tunisia  
Dorra Mallouli, ENIS, Tunisia  
Imen Ghorbel, ENETCom, Tunisia  
Manel Euch, ENIS, Tunisia  
Rahma Aloulou, CRNS, Tunisia  
Tarek Frikha, ENIS, Tunisia  
Yassin Aydi, ENIS, Tunisia



The International Conference on Microelectronics (ICM) has been held in numerous countries across the Middle East and Asia over the past 29 years. The 30<sup>th</sup> edition of the conference will be held in Tunisia.

ICM 2018 will include oral, poster sessions and tutorials given by experts in state-of-the-art topics. The regular technical program will run for three days. In addition, tutorial sessions will be held on the first day of the conference. Papers are solicited in, but not limited to, the following topics

Circuits and Systems	CAD Tools and Design	Micro/Nanoelectronics
<ol style="list-style-type: none"> <li>1. Analog and RF circuit design techniques</li> <li>2. Digital signal and data processing</li> <li>3. Wireless com. systems</li> <li>4. Nonlinear circuits</li> <li>5. System on Chip</li> <li>6. VLSI for Signal and Image Processing</li> <li>7. Integrated antenna and front-end co-design</li> <li>8. Signal Processing in Communications.</li> </ol>	<ol style="list-style-type: none"> <li>1. Simulation (process, device, circuit, logic, timing, functional)</li> <li>2. Layout (plac., rout., floor plan., symbolic, ERC, DRC)</li> <li>3. Silicon optimization</li> <li>4. Parallel embedded systems</li> <li>5. Testing: Formal verification</li> <li>6. CAD for FPGAs</li> <li>7. High level synthesis tools</li> <li>8. Design for testability.</li> </ol>	<ol style="list-style-type: none"> <li>1. Device characterization and modelling</li> <li>2. Device physics and novel structures</li> <li>3. Process technology CMOS, BJT,</li> <li>4. BiCMOS, GaAs</li> <li>5. Reliability and failure analysis</li> <li>6. Optoelectronics</li> <li>7. MEMS Devices</li> <li>8. Packaging and surface mount technology.</li> </ol>

Perspective authors are invited to submit full-length (4 pages) papers, in IEEE format, using the guidelines in the authors' info. Only electronic submissions will be accepted via the online submission system. Accepted papers will be published in the electronic Conference Proceedings (CD ROM) and included in IEEE Xplore database. Authors of the best papers will be invited to resubmit an extended version to be considered for publication at an International Journal.

## Deadlines:

- Full Paper Submission : **July 15, 2018**
- Notification of Acceptance: **September 16, 2018**
- Camera Ready Submission: **October 11, 2018**

## For any information, contact:

### General co-Chairs

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### International Coordinators

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Fouad El Haj Hassan, LU, Lebanon  
Khaled Salama, KAUST, KSA  
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Said Belkouch, UCA, Morocco  
Montassar Maamoun, Blida, Algeria  
Abbes Amira, Qatar Univ, Qatar  
Abdoul Rjoub, JUST, Jordan  
Ahmed Amari, SQ Univ. Oman  
Mohab Anis, AUC, Egypt  
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