

The 30th International Conference on Microelectronics













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16-19 December 2018, Tunisia

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Call For Tutorials Proposals

The ICM 2018 Conference will feature a state-of-the-art set of tutorial presentations. The Organizing Committee invites proposals and ideas for tutorials to be held as part of the Conference.

Tutorials will provide a good opportunity for conference attendees to learn from leaders in the field, explore cutting-edge topics, and acquire new insights, knowledge and skills.

The topic should be of sufficient relevance with respect to the general scope of the conference:

1. Analog and RF circuit design techniques 2. Digital signal and data processing 3. Wireless com. systems 4. Nonlinear circuits 5. System on Chip 6. VLSI for Signal and Image Processing 7. Integrated antenna and front-end co-design 6. Signal Processing in 7. Simulation (process, device, circuit, logic, and modelling 7. Integrated antenna and front-end co-design in serior and serior circuit, logic, and modelling 7. Device characterization and modelling 7. Device physics and novel structures 8. Signal Processing in serior plan., symbolic, ERC, DRC) 8. Signal Processing 9. Layout (plac., rout., floor plan., symbolic, ERC, DRC) 9. Signal embedded systems 9. Testing: Formal processing in serior plan., symbolic, ERC, DRC) 9. Signal embedded systems 9. Reliability and failure analysis 9. Optoelectronics 9. MEMS Devices 9. Packaging and surface propert technology 9. Packaging and surface propert technology 9. Device characterization and modelling 9. Device physics and novel structures 9. Process techno-logy, CMOS, BJT, 9. BiCMOS, GaAs 9. Reliability and failure analysis 9. Optoelectronics 9. Packaging and surface propert technology	Circuits and Systems	CAD Tools and Design	Micro/Nanoelectronics
Communications.	design techniques 2. Digital signal and data processing 3. Wireless com. systems 4. Nonlinear circuits 5. System on Chip 6. VLSI for Signal and Image Processing 7. Integrated antenna and front-end co-design	device, circuit, logic, timing, functional) 2. Layout (plac., rout., floor plan., symbolic, ERC, DRC) 3. Silicon optimization 4. Parallel embedded systems 5. Testing: Formal verification 6. CAD for FPGAs 7. High level synthesis tools	and modelling 2. Device physics and novel structures 3. Process techno-logy, CMOS, BJT, 4. BiCMOS, GaAs 5. Reliability and failure analysis 6. Optoelectronics 7. MEMS Devices

Submission: Prospective tutorial speakers are invited to submit their proposals via e-mail in a single PDF file to Tutorials Co-chairs:

Mohamed Atri: mohamed.atri@fsm.rnu.tn

Mohammad Baker: baker.mohammad@kustar.ac.ae

A. Al Maashari: amaashari@squ.edu.om

Each Tutorial proposal should describe concisely the content, importance and timeliness of the tutorial. It should contain the following sections:

- Tutorial's title.
- Tutorial's type (introductory, intermediate, or advanced).
- Summary describing the main focus and topics to be addressed and explaining why the tutorial is appropriate for ICM 2018.
- A detailed outline of the presentation.
- A description of the proposer's lecturing expertise.
- The proposed duration of the tutorial.

Deadlines:

Tutorials proposals due: August 10, August 20, 2018 (Extended)



















